

**Notice of References Cited**

Application/Control No.

09/495,150

Applicant(s)/Patent Under  
Reexamination  
HEGDE ET AL.

Examiner

Herng-der Day

Art Unit

2123

Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,343,463 ✓	08-1994	van Tetering et al.	370/253
	B	US-5,450,400 ✓	09-1995	Denissen et al.	370/391
	C	US-5,838,948 ✓	11-1998	Bunza, Geoffrey J.	703/27
	D	US-5,870,588 ✓	02-1999	Rompaey et al.	703/13
	E	US-5,960,182 ✓	09-1999	Matsuoka et al.	703/17
	F	US-6,202,044 ✓	03-2001	Tzori, Yiftach	703/28
	G	US-6,212,489 ✓	04-2001	Klein et al.	703/13
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Becker et al., "An Engineering Environment for Hardware/Software Co-Simulation", Proceedings of the 29th ACM/IEEE Conference on Design Automation Conference, 1992, pages 129-134. ✓
	V	Bauer et al., "Hardware/Software Co-Simulation in a VHDL-based Test Bench Approach", Proceedings of the 34th Design Automation Conference, June 1997, pages 774-779. ✓
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.